

**Notice of References Cited**

Application/Control No.

09/800,145

Applicant(s)/Patent Under  
Reexamination  
DENT ET AL.

Examiner

Paula W Klimach

Art Unit

2135

Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-			
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	D	US-			
	E	US-			
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	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wey et al., Design and Analysis of VLSI-based parallel Multipliers, July 1990, IEEE, Volume 137 pages 328-329
	V	Heikes, A 4.5 mm Multiplier Array for 200MFLOP Pipelined Coprocessor, 1994, IEEE, page 290
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.